

Automotive Dual N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY

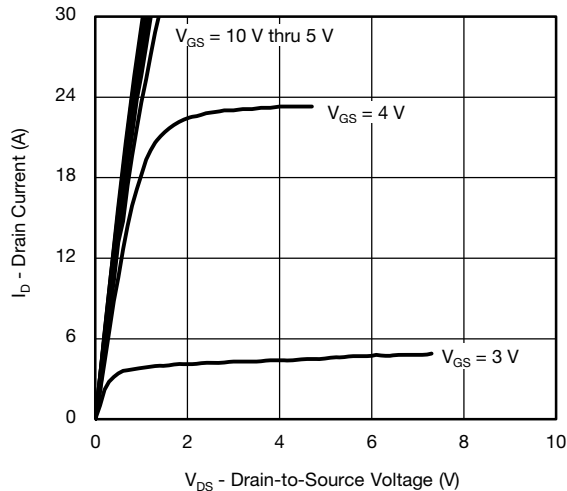
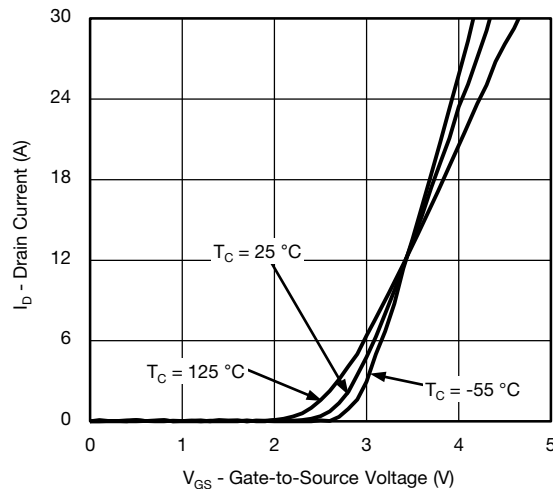
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.036
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.046
I_D (A) per leg	8
Configuration	Dual
Package	PowerPAK SO-8L

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101

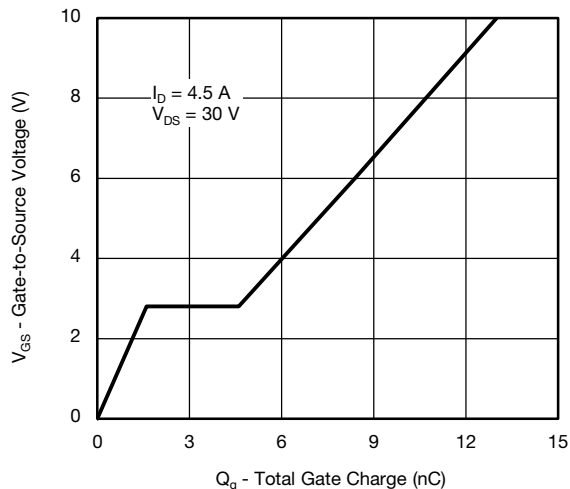


SPECIFICATIONS ($T_C = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = 250\text{ }\mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$					

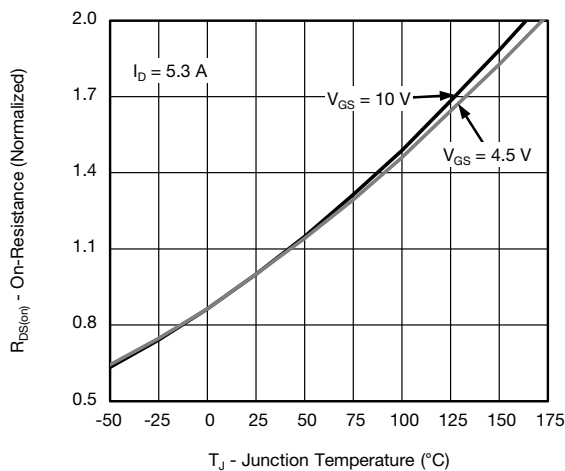
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics


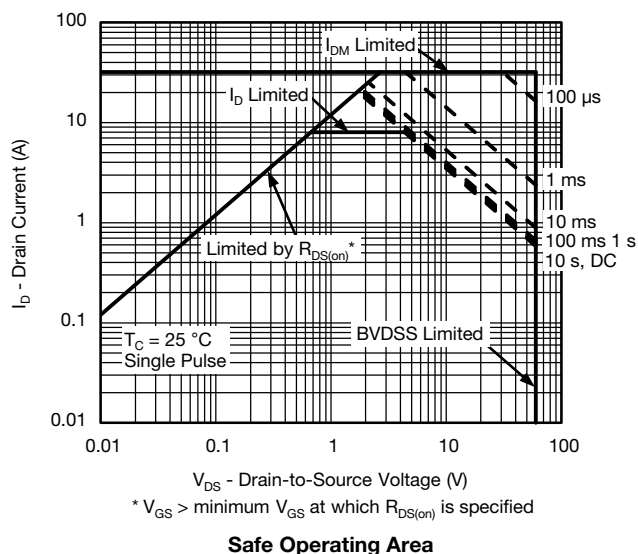


TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



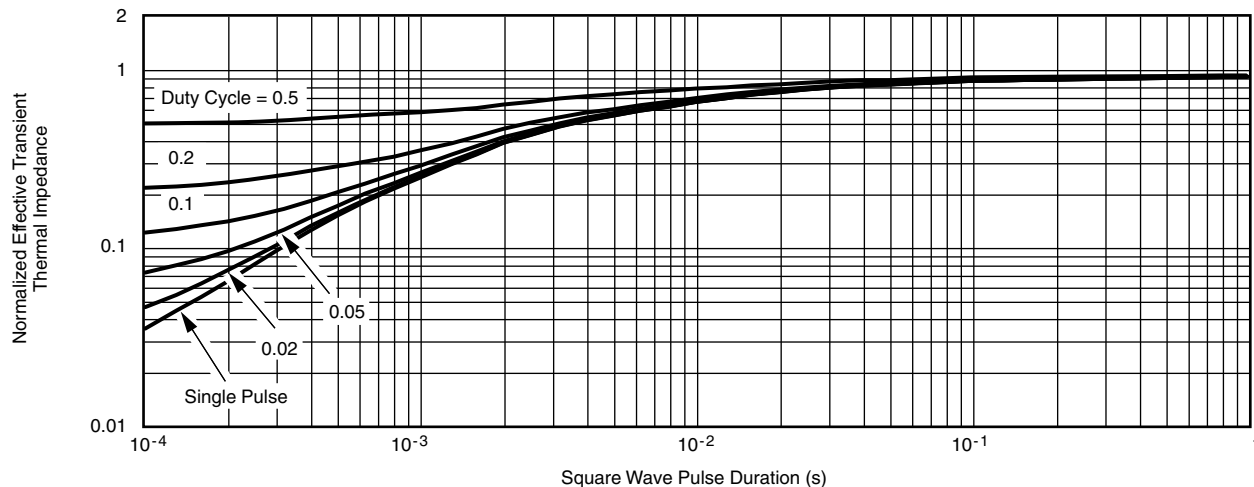
Gate Charge



THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)




THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal



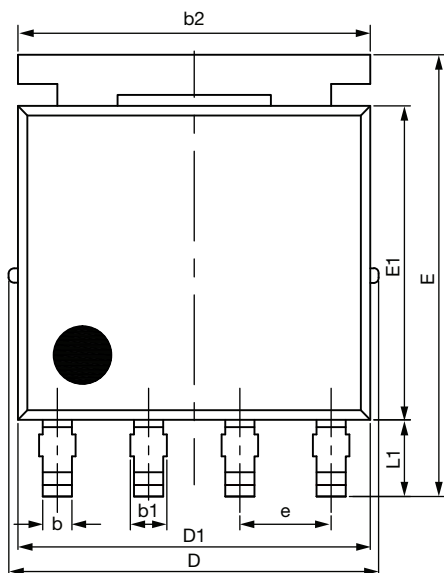
REVISION HISTORY ^a		
REVISION	DATE	DESCRIPTION OF CHANGE
D	04-Aug-15	• Revised R_g minimum limit

Note

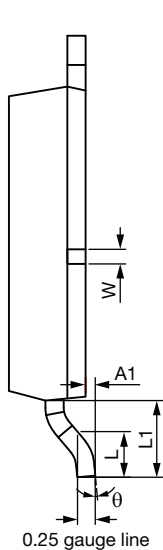
a. As of April 2014



PowerPAK® SO-8L Case Outline 1



Topside view

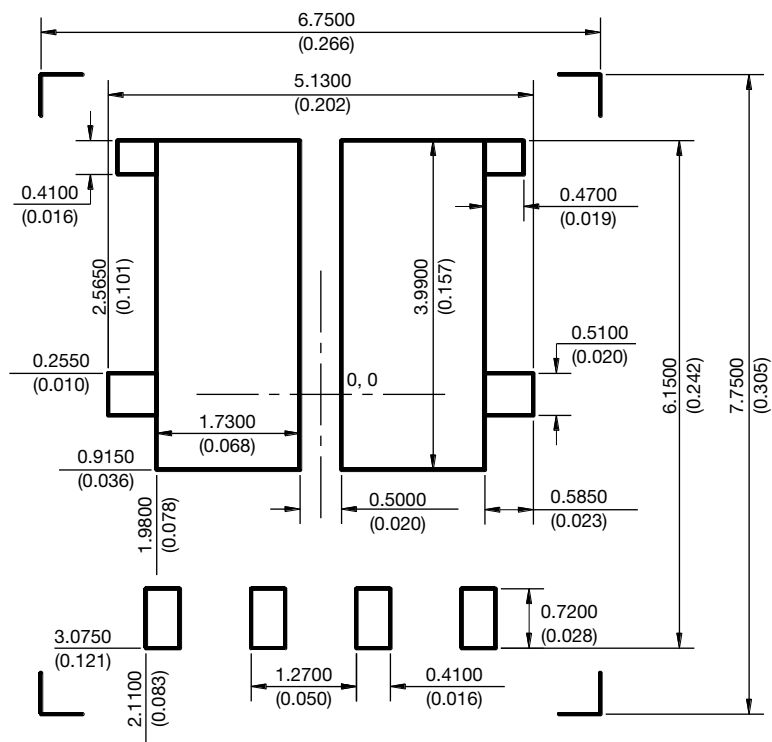




DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.00	1.07	1.14	0.039	0.042	0.045
A1	0.00	-	0.127	0.00	-	0.005
b	0.33	0.41	0.48	0.013	0.016	0.019
b1	0.44	0.51	0.58	0.017	0.020	0.023
b2	4.80	4.90	5.00	0.189	0.193	0.19



RECOMMENDED MINIMUM PAD FOR PowerPAK® SO-8L DUAL





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