

# 12-Bit Binary Counter

## MC14040B

The MC14040B 12-stage binary counter is constructed with MOS P-Channel and N-Channel enhancement mode devices in a single monolithic structure. This part is designed with an input wave shaping circuit and 12 stages of ripple-carry binary counter. The device advances the count on the negative-going edge of the clock pulse. Applications include time delay circuits, counter controls, and frequency-driving circuits.

### Features

- Fully Static Operation
- Diode Protection on All Inputs
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range
- Common Reset Line
- Pin-for-Pin Replacement for CD4040B
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

### MAXIMUM RATINGS (Voltages Referenced to V<sub>SS</sub>)

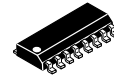
| Symbol                             | Parameter                                         | Value                         | Unit |
|------------------------------------|---------------------------------------------------|-------------------------------|------|
| V <sub>DD</sub>                    | DC Supply Voltage Range                           | -0.5 to +18.0                 | V    |
| V <sub>in</sub> , V <sub>out</sub> | Input or Output Voltage Range (DC or Transient)   | -0.5 to V <sub>DD</sub> + 0.5 | V    |
| I <sub>in</sub> , I <sub>out</sub> | Input or Output Current (DC or Transient) per Pin | ±10                           | mA   |
| P <sub>D</sub>                     | Power Dissipation, per Package (Note 1)           | 500                           | mW   |
| T <sub>A</sub>                     | Ambient Temperature Range                         | -55 to +125                   | °C   |
| T <sub>stg</sub>                   | Storage Temperature Range                         | -65 to +150                   | °C   |
| T <sub>L</sub>                     | Lead Temperature (8-Second Soldering)             | 260                           | °C   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Temperature Derating: "D/DW" Packages: -7.0 mW/°C From 65°C To 125°C

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V<sub>in</sub> and V<sub>out</sub> should be constrained to the range V<sub>SS</sub> ≤ (V<sub>in</sub> or V<sub>out</sub>) ≤ V<sub>DD</sub>.

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either V<sub>SS</sub> or V<sub>DD</sub>). Unused outputs must be left open.



SOIC-16  
D SUFFIX  
CASE 751B

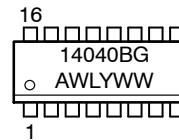


TSSOP-16  
DT SUFFIX  
CASE 948F

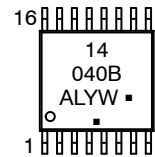
### PIN ASSIGNMENT

|                 |   |    |                 |
|-----------------|---|----|-----------------|
| Q12             | 1 | 16 | V <sub>DD</sub> |
| Q6              | 2 | 15 | Q11             |
| Q5              | 3 | 14 | Q10             |
| Q7              | 4 | 13 | Q8              |
| Q4              | 5 | 12 | Q9              |
| Q3              | 6 | 11 | R               |
| Q2              | 7 | 10 | C               |
| V <sub>SS</sub> | 8 | 9  | Q1              |

### MARKING DIAGRAMS



SOIC-16



TSSOP-16

A = Assembly Location  
 WL, L = Wafer Lot  
 YY, Y = Year  
 WW, W = Work Week  
 G or ■ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

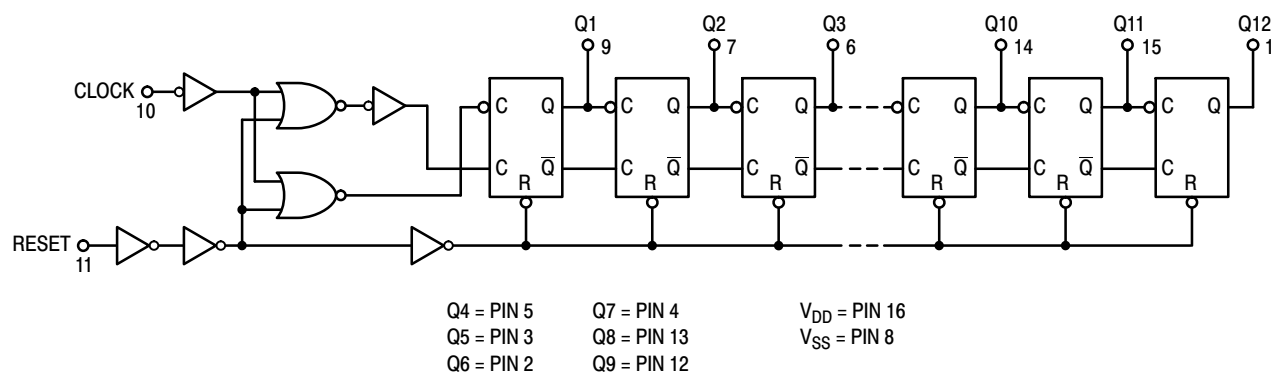
**MC14040B**

### TRUTH TABLE

| Clock                                                                             | Reset | Output State          |
|-----------------------------------------------------------------------------------|-------|-----------------------|
|  | 0     | No Change             |
|  | 0     | Advance to next state |
| X                                                                                 | 1     | All Outputs are low   |

X = Don't Care

### LOGIC DIAGRAM



## ORDERING INFORMATION

| Device          | Package               | Shipping <sup>†</sup>    |
|-----------------|-----------------------|--------------------------|
| MC14040BDG      | SOIC–16<br>(Pb–Free)  | 48 Units / Rail          |
| MC14040BDR2G    | SOIC–16<br>(Pb–Free)  | 2500 Units / Tape & Reel |
| NLV14040BDR2G*  | SOIC–16<br>(Pb–Free)  | 2500 Units / Tape & Reel |
| MC14040BDTR2G   | TSSOP–16<br>(Pb–Free) | 2500 Units / Tape & Reel |
| NLV14040BDTR2G* | TSSOP–16<br>(Pb–Free) | 2500 Units / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

# MC14040B

## ELECTRICAL CHARACTERISTICS (Voltages Referenced to V<sub>SS</sub>)

| Characteristic                                                                                                                                                                                                                                     | Symbol                       | V <sub>DD</sub><br>Vdc | -55°C                                                                                                                                                          |      | 25°C  |                 |      | 125°C |      | Unit |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------|------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|------|-------|-----------------|------|-------|------|------|
|                                                                                                                                                                                                                                                    |                              |                        | Min                                                                                                                                                            | Max  | Min   | Typ<br>(Note 2) | Max  | Min   | Max  |      |
| Output Voltage<br>V <sub>in</sub> = V <sub>DD</sub> or 0                                                                                                                                                                                           | "0" Level<br>V <sub>OL</sub> | 5.0                    | –                                                                                                                                                              | 0.05 | –     | 0               | 0.05 | –     | 0.05 | Vdc  |
|                                                                                                                                                                                                                                                    |                              | 10                     | –                                                                                                                                                              | 0.05 | –     | 0               | 0.05 | –     | 0.05 |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | –                                                                                                                                                              | 0.05 | –     | 0               | 0.05 | –     | 0.05 |      |
|                                                                                                                                                                                                                                                    | "1" Level<br>V <sub>OH</sub> | 5.0                    | 4.95                                                                                                                                                           | –    | 4.95  | 5.0             | –    | 4.95  | –    | Vdc  |
|                                                                                                                                                                                                                                                    |                              | 10                     | 9.95                                                                                                                                                           | –    | 9.95  | 10              | –    | 9.95  | –    |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | 14.95                                                                                                                                                          | –    | 14.95 | 15              | –    | 14.95 | –    |      |
| Input Voltage<br>(V <sub>O</sub> = 4.5 or 0.5 Vdc)<br>(V <sub>O</sub> = 9.0 or 1.0 Vdc)<br>(V <sub>O</sub> = 13.5 or 1.5 Vdc)                                                                                                                      | "0" Level<br>V <sub>IL</sub> | 5.0                    | –                                                                                                                                                              | 1.5  | –     | 2.25            | 1.5  | –     | 1.5  | Vdc  |
|                                                                                                                                                                                                                                                    |                              | 10                     | –                                                                                                                                                              | 3.0  | –     | 4.50            | 3.0  | –     | 3.0  |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | –                                                                                                                                                              | 4.0  | –     | 6.75            | 4.0  | –     | 4.0  |      |
|                                                                                                                                                                                                                                                    | "1" Level<br>V <sub>IH</sub> | 5.0                    | 3.5                                                                                                                                                            | –    | 3.5   | 2.75            | –    | 3.5   | –    | Vdc  |
|                                                                                                                                                                                                                                                    |                              | 10                     | 7.0                                                                                                                                                            | –    | 7.0   | 5.50            | –    | 7.0   | –    |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | 11                                                                                                                                                             | –    | 11    | 8.25            | –    | 11    | –    |      |
| Output Drive Current<br>(V <sub>OH</sub> = 2.5 Vdc)<br>(V <sub>OH</sub> = 4.6 Vdc)<br>(V <sub>OH</sub> = 9.5 Vdc)<br>(V <sub>OH</sub> = 13.5 Vdc)<br><br>(V <sub>OL</sub> = 0.4 Vdc)<br>(V <sub>OL</sub> = 0.5 Vdc)<br>(V <sub>OL</sub> = 1.5 Vdc) | Source<br>I <sub>OH</sub>    | 5.0                    | –3.0                                                                                                                                                           | –    | –2.4  | –4.2            | –    | –1.7  | –    | mAdc |
|                                                                                                                                                                                                                                                    |                              | 5.0                    | –0.64                                                                                                                                                          | –    | –0.51 | –0.88           | –    | –0.36 | –    |      |
|                                                                                                                                                                                                                                                    |                              | 10                     | –1.6                                                                                                                                                           | –    | –1.3  | –2.25           | –    | –0.9  | –    |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | –4.2                                                                                                                                                           | –    | –3.4  | –8.8            | –    | –2.4  | –    |      |
|                                                                                                                                                                                                                                                    | Sink<br>I <sub>OL</sub>      | 5.0                    | 0.64                                                                                                                                                           | –    | 0.51  | 0.88            | –    | 0.36  | –    | mAdc |
|                                                                                                                                                                                                                                                    |                              | 10                     | 1.6                                                                                                                                                            | –    | 1.3   | 2.25            | –    | 0.9   | –    |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | 4.2                                                                                                                                                            | –    | 3.4   | 8.8             | –    | 2.4   | –    |      |
| Input Current                                                                                                                                                                                                                                      | I <sub>in</sub>              | 15                     | –                                                                                                                                                              | ±0.1 | –     | ±0.00001        | ±0.1 | –     | ±1.0 | μAdc |
| Input Capacitance<br>(V <sub>in</sub> = 0)                                                                                                                                                                                                         | C <sub>in</sub>              | –                      | –                                                                                                                                                              | –    | –     | 5.0             | 7.5  | –     | –    | pF   |
| Quiescent Current<br>(Per Package)                                                                                                                                                                                                                 | I <sub>DD</sub>              | 5.0                    | –                                                                                                                                                              | 5.0  | –     | 0.005           | 5.0  | –     | 150  | μAdc |
|                                                                                                                                                                                                                                                    |                              | 10                     | –                                                                                                                                                              | 10   | –     | 0.010           | 10   | –     | 300  |      |
|                                                                                                                                                                                                                                                    |                              | 15                     | –                                                                                                                                                              | 20   | –     | 0.015           | 20   | –     | 600  |      |
| Total Supply Current (Notes 3 & 4)<br>(Dynamic plus Quiescent,<br>Per Package)<br>(C <sub>L</sub> = 50 pF on all outputs, all<br>buffers switching)                                                                                                | I <sub>T</sub>               | 5.0<br>10<br>15        | I <sub>T</sub> = (0.42 μA/kHz) f + I <sub>DD</sub><br>I <sub>T</sub> = (0.85 μA/kHz) f + I <sub>DD</sub><br>I <sub>T</sub> = (1.43 μA/kHz) f + I <sub>DD</sub> |      |       |                 |      |       |      | μAdc |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

3. The formulas given are for the typical characteristics only at 25°C.

4. To calculate total supply current at loads other than 50 pF:

$$I_T(C_L) = I_T(50 \text{ pF}) + (C_L - 50) \text{ Vfk}$$

where: I<sub>T</sub> is in μA (per package), C<sub>L</sub> in pF, V = (V<sub>DD</sub> – V<sub>SS</sub>) in volts, f in kHz is input frequency, and k = 0.001.

# MC14040B

## SWITCHING CHARACTERISTICS (Note 5) ( $C_L = 50$ pF, $T_A = 25^\circ\text{C}$ )

| Characteristic                                                                                                                                                                                                                           | Symbol                  | $V_{DD}$<br>Vdc | Min               | Typ<br>(Note 6)    | Max                  | Unit |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------|-----------------|-------------------|--------------------|----------------------|------|
| Output Rise and Fall Time<br>$T_{TLH}, T_{THL} = (1.5 \text{ ns/pF}) C_L + 25 \text{ ns}$<br>$T_{TLH}, T_{THL} = (0.75 \text{ ns/pF}) C_L + 12.5 \text{ ns}$<br>$T_{TLH}, T_{THL} = (0.55 \text{ ns/pF}) C_L + 9.5 \text{ ns}$           | $t_{TLH},$<br>$t_{THL}$ | 5.0<br>10<br>15 | –<br>–<br>–       | 100<br>50<br>40    | 200<br>100<br>80     | ns   |
| Propagation Delay Time<br>Clock to Q1<br>$t_{PHL}, t_{PLH} = (1.7 \text{ ns/pF}) C_L + 315 \text{ ns}$<br>$t_{PHL}, t_{PLH} = (0.66 \text{ ns/pF}) C_L + 137 \text{ ns}$<br>$t_{PHL}, t_{PLH} = (0.5 \text{ ns/pF}) C_L + 95 \text{ ns}$ | $t_{PLH},$<br>$t_{PHL}$ | 5.0<br>10<br>15 | –<br>–<br>–       | 260<br>115<br>80   | 520<br>230<br>160    | ns   |
| Clock to Q12<br>$t_{PHL}, t_{PLH} = (1.7 \text{ ns/pF}) C_L + 2415 \text{ ns}$<br>$t_{PHL}, t_{PLH} = (0.66 \text{ ns/pF}) C_L + 867 \text{ ns}$<br>$t_{PHL}, t_{PLH} = (0.5 \text{ ns/pF}) C_L + 475 \text{ ns}$                        |                         | 5.0<br>10<br>15 | –<br>–<br>–       | 1625<br>720<br>500 | 3250<br>1440<br>1000 | ns   |
| Propagation Delay Time<br>Reset to $Q_n$<br>$t_{PHL} = (1.7 \text{ ns/pF}) C_L + 485 \text{ ns}$<br>$t_{PHL} = (0.86 \text{ ns/pF}) C_L + 182 \text{ ns}$<br>$t_{PHL} = (0.5 \text{ ns/pF}) C_L + 145 \text{ ns}$                        | $t_{PHL}$               | 5.0<br>10<br>15 | –<br>–<br>–       | 370<br>155<br>115  | 740<br>310<br>230    | ns   |
| Clock Pulse Width                                                                                                                                                                                                                        | $t_{WH}$                | 5.0<br>10<br>15 | 385<br>150<br>115 | 140<br>55<br>38    | –<br>–<br>–          | ns   |
| Clock Pulse Frequency                                                                                                                                                                                                                    | $f_{cl}$                | 5.0<br>10<br>15 | –<br>–<br>–       | 2.1<br>7.0<br>10.0 | 1.5<br>3.5<br>4.5    | MHz  |
| Clock Rise and Fall Time                                                                                                                                                                                                                 | $t_{TLH}, t_{THL}$      | 5.0<br>10<br>15 | No Limit          |                    |                      | ns   |
| Reset Pulse Width                                                                                                                                                                                                                        | $t_{WH}$                | 5.0<br>10<br>15 | 960<br>360<br>270 | 320<br>120<br>80   | –<br>–<br>–          | ns   |
| Reset Removal Time                                                                                                                                                                                                                       | $t_{rem}$               | 5.0<br>10<br>15 | 130<br>50<br>30   | 65<br>25<br>15     | –<br>–<br>–          | ns   |

5. The formulas given are for the typical characteristics only at  $25^\circ\text{C}$ .

6. Data labelled "Typ" is not to be used for design purposes but is intended as an indication of the IC's potential performance.

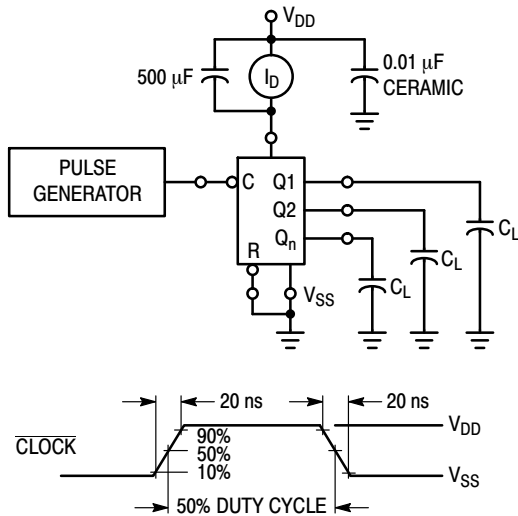


Figure 1. Power Dissipation Test Circuit and Waveform

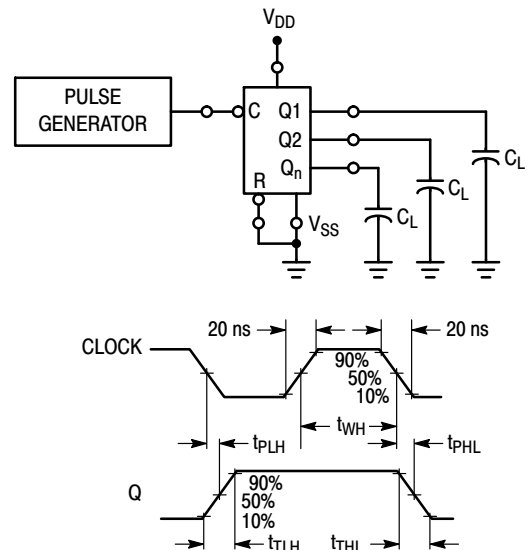


Figure 2. Switching Time Test Circuit and Waveforms

## MC14040B

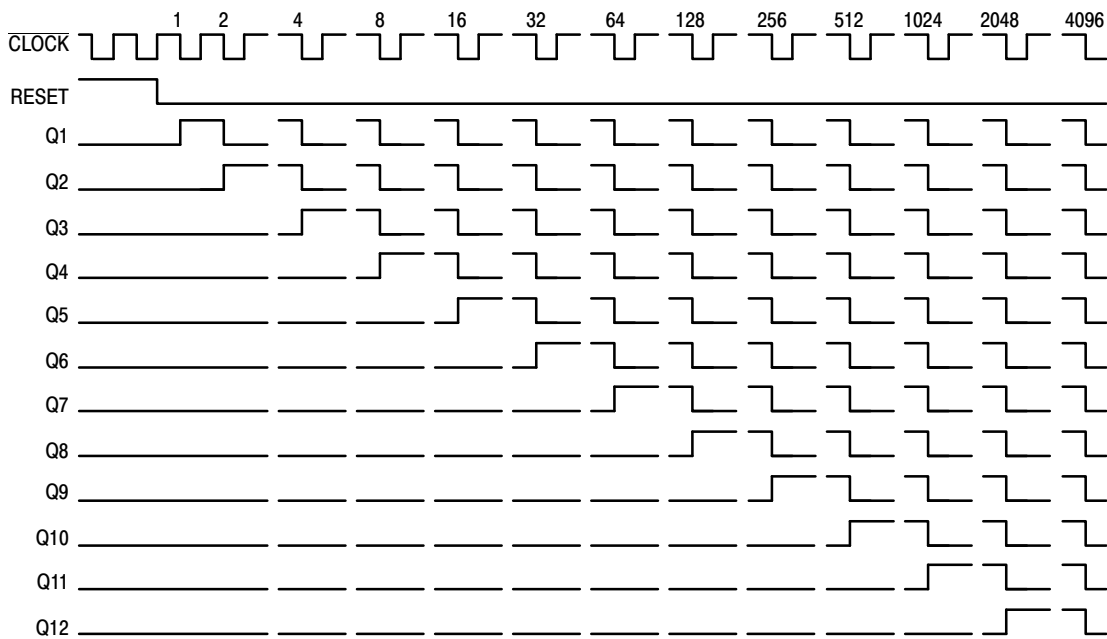


Figure 3. Timing Diagram

## APPLICATIONS INFORMATION

### TIME-BASE GENERATOR

A 60 Hz sinewave obtained through a 1.0 Megohm resistor connected directly to a standard 120 Vac power line is applied to the clock input of the MC14040B. By selecting

outputs Q5, Q10, Q11, and Q12 division by 3600 is accomplished. The MC14012B decodes the counter outputs, produces a single output pulse, and resets the binary counter. The resulting output frequency is 1.0 pulse/minute.

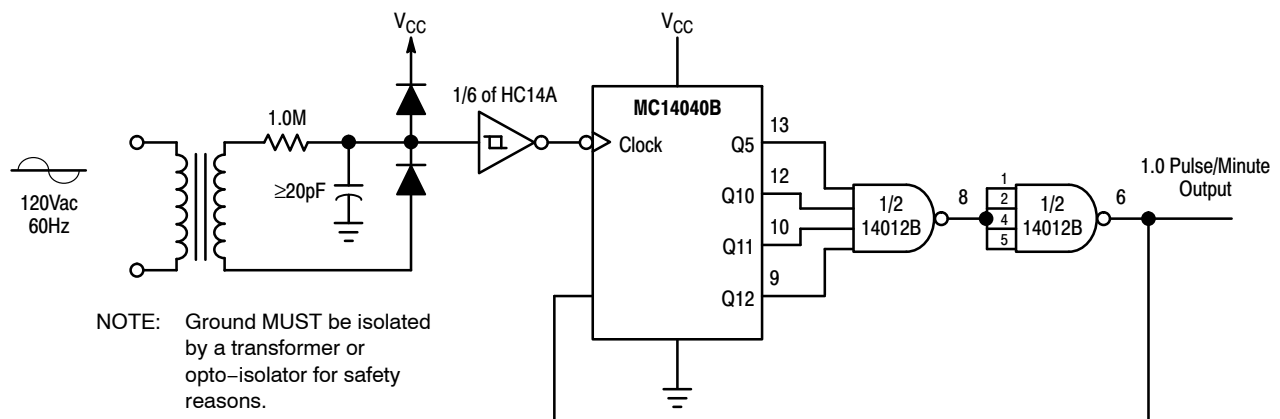


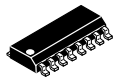
Figure 4. Time-Base Generator

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®

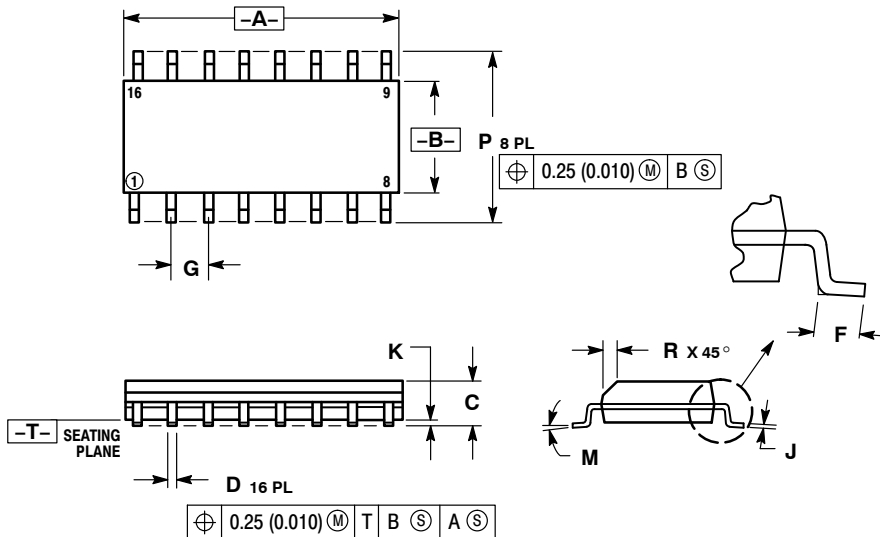
ON



SCALE 1:1

### SOIC-16 CASE 751B-05 ISSUE K

DATE 29 DEC 2006



#### NOTES:

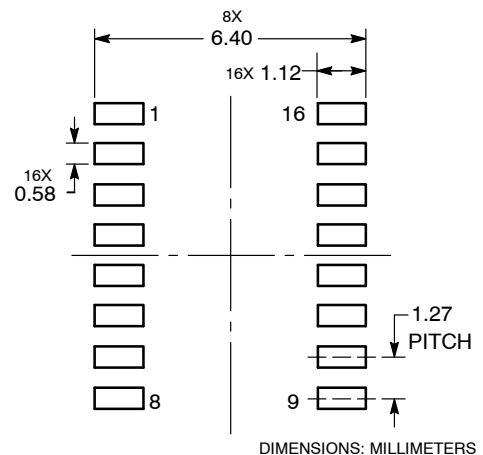
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |       | INCHES    |       |
|-----|-------------|-------|-----------|-------|
|     | MIN         | MAX   | MIN       | MAX   |
| A   | 9.80        | 10.00 | 0.386     | 0.393 |
| B   | 3.80        | 4.00  | 0.150     | 0.157 |
| C   | 1.35        | 1.75  | 0.054     | 0.068 |
| D   | 0.35        | 0.49  | 0.014     | 0.019 |
| E   | 0.40        | 1.25  | 0.016     | 0.049 |
| F   | 1.27 BSC    |       | 0.050 BSC |       |
| G   | 0.19        | 0.25  | 0.008     | 0.009 |
| H   | 0.10        | 0.25  | 0.004     | 0.009 |
| I   | 0°          | 7°    | 0°        | 7°    |
| J   | 5.80        | 6.20  | 0.229     | 0.244 |
| K   | 0.25        | 0.50  | 0.010     | 0.019 |

|                   |                   |                          |                          |
|-------------------|-------------------|--------------------------|--------------------------|
| STYLE 1:          | STYLE 2:          | STYLE 3:                 | STYLE 4:                 |
| PIN 1. COLLECTOR  | PIN 1. CATHODE    | PIN 1. COLLECTOR, DYE #1 | PIN 1. COLLECTOR, DYE #1 |
| 2. BASE           | 2. ANODE          | 2. BASE, #1              | 2. COLLECTOR, #1         |
| 3. EMITTER        | 3. NO CONNECTION  | 3. EMITTER, #1           | 3. COLLECTOR, #2         |
| 4. NO CONNECTION  | 4. CATHODE        | 4. COLLECTOR, #1         | 4. COLLECTOR, #2         |
| 5. EMITTER        | 5. CATHODE        | 5. COLLECTOR, #2         | 5. COLLECTOR, #3         |
| 6. BASE           | 6. NO CONNECTION  | 6. BASE, #2              | 6. COLLECTOR, #3         |
| 7. COLLECTOR      | 7. ANODE          | 7. EMITTER, #2           | 7. COLLECTOR, #4         |
| 8. COLLECTOR      | 8. CATHODE        | 8. COLLECTOR, #2         | 8. COLLECTOR, #4         |
| 9. BASE           | 9. CATHODE        | 9. COLLECTOR, #3         | 9. BASE, #4              |
| 10. EMITTER       | 10. ANODE         | 10. BASE, #3             | 10. EMITTER, #4          |
| 11. NO CONNECTION | 11. NO CONNECTION | 11. EMITTER, #3          | 11. BASE, #3             |
| 12. EMITTER       | 12. CATHODE       | 12. COLLECTOR, #3        | 12. EMITTER, #3          |
| 13. BASE          | 13. CATHODE       | 13. COLLECTOR, #4        | 13. BASE, #2             |
| 14. COLLECTOR     | 14. NO CONNECTION | 14. BASE, #4             | 14. EMITTER, #2          |
| 15. EMITTER       | 15. ANODE         | 15. EMITTER, #4          | 15. BASE, #1             |
| 16. COLLECTOR     | 16. CATHODE       | 16. COLLECTOR, #4        | 16. EMITTER, #1          |

|                      |                |                           |
|----------------------|----------------|---------------------------|
| STYLE 5:             | STYLE 6:       | STYLE 7:                  |
| PIN 1. DRAIN, DYE #1 | PIN 1. CATHODE | PIN 1. SOURCE N-CH        |
| 2. DRAIN, #1         | 2. CATHODE     | 2. COMMON DRAIN (OUTPUT)  |
| 3. DRAIN, #2         | 3. CATHODE     | 3. COMMON DRAIN (OUTPUT)  |
| 4. DRAIN, #2         | 4. CATHODE     | 4. GATE P-CH              |
| 5. DRAIN, #3         | 5. CATHODE     | 5. COMMON DRAIN (OUTPUT)  |
| 6. DRAIN, #3         | 6. CATHODE     | 6. COMMON DRAIN (OUTPUT)  |
| 7. DRAIN, #4         | 7. CATHODE     | 7. COMMON DRAIN (OUTPUT)  |
| 8. DRAIN, #4         | 8. CATHODE     | 8. SOURCE P-CH            |
| 9. GATE, #4          | 9. ANODE       | 9. SOURCE P-CH            |
| 10. SOURCE, #4       | 10. ANODE      | 10. COMMON DRAIN (OUTPUT) |
| 11. GATE, #3         | 11. ANODE      | 11. COMMON DRAIN (OUTPUT) |
| 12. SOURCE, #3       | 12. ANODE      | 12. COMMON DRAIN (OUTPUT) |
| 13. GATE, #2         | 13. ANODE      | 13. GATE N-CH             |
| 14. SOURCE, #2       | 14. ANODE      | 14. COMMON DRAIN (OUTPUT) |
| 15. GATE, #1         | 15. ANODE      | 15. COMMON DRAIN (OUTPUT) |
| 16. SOURCE, #1       | 16. ANODE      | 16. SOURCE N-CH           |

#### SOLDERING FOOTPRINT



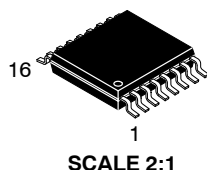
DIMENSIONS: MILLIMETERS

|                  |             |                                                                                                                                                                                     |
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| DESCRIPTION:     | SOIC-16     | PAGE 1 OF 1                                                                                                                                                                         |

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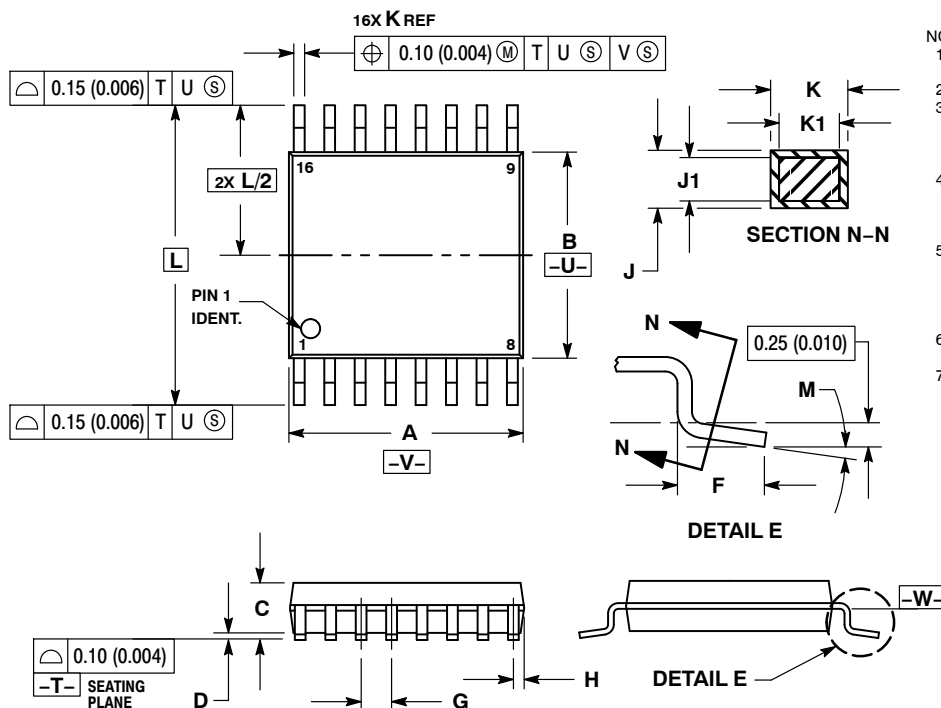
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



**TSSOP-16**  
CASE 948F-01  
ISSUE B

DATE 19 OCT 2006

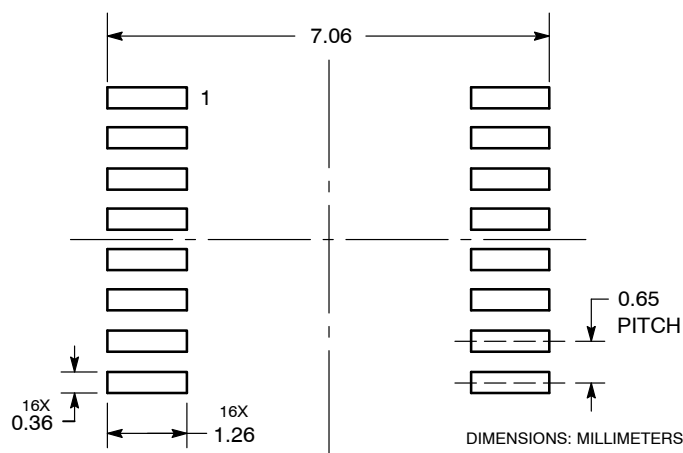


## NOTES:

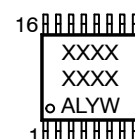
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH. PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 4.90        | 5.10 | 0.193     | 0.200 |
| B   | 4.30        | 4.50 | 0.169     | 0.177 |
| C   | ---         | 1.20 | ---       | 0.047 |
| D   | 0.05        | 0.15 | 0.002     | 0.006 |
| F   | 0.50        | 0.75 | 0.020     | 0.030 |
| G   | 0.65 BSC    |      | 0.026 BSC |       |
| H   | 0.18        | 0.28 | 0.007     | 0.011 |
| J   | 0.09        | 0.20 | 0.004     | 0.008 |
| J1  | 0.09        | 0.16 | 0.004     | 0.006 |
| K   | 0.19        | 0.30 | 0.007     | 0.012 |
| K1  | 0.19        | 0.25 | 0.007     | 0.010 |
| L   | 6.40 BSC    |      | 0.252 BSC |       |
| M   | 0°          | 8°   | 0°        | 8°    |

## SOLDERING FOOTPRINT



## GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code  
A = Assembly Location  
L = Wafer Lot  
Y = Year  
W = Work Week  
G or ■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present.

|                  |             |                                                                                                                                                                                  |
|------------------|-------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| DESCRIPTION:     | TSSOP-16    | PAGE 1 OF 1                                                                                                                                                                      |

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